

Separation Logic 4/4

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Inria Paris

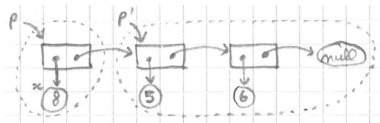
February 9, 2023

part 4/4 : some material from Arthur Charguéraud

Chapter 19

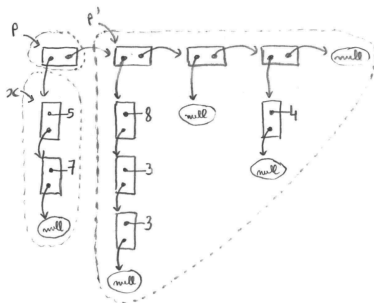
Higher-order representation predicates and the access problem

Specification of construction, for basic values



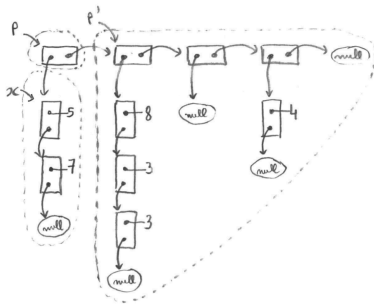
$$\{p' \rightsquigarrow \text{MList } L\} (\text{cons } x \ p') \ \{\lambda p. \ p \rightsquigarrow \text{MList } (x :: L)\}$$

Specification of construction



$$\{x \rightsquigarrow R X * p' \rightsquigarrow \text{Mlistof } R L\} (\text{cons } x p') \{\lambda p. p \rightsquigarrow \text{Mlistof } R (X :: L)\}$$

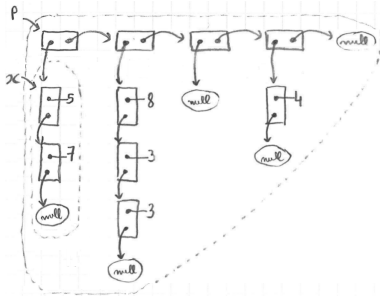
Specification of deconstruction



$$\{p \rightsquigarrow \text{Mlistof } R(X :: L)\} (\text{uncons } p)$$

$$\{\lambda(x, p'). x \rightsquigarrow R X * p' \rightsquigarrow \text{Mlistof } R L\}$$

Specification of accesses: the problem

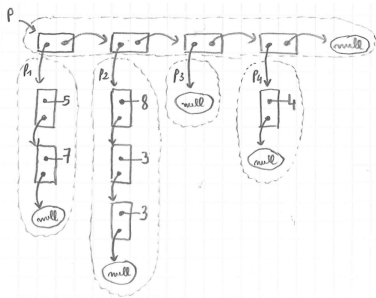


Incorrect specification for head:

$$\{p \rightsquigarrow \text{Mlistof } R(X :: L)\} (\text{head } p)$$

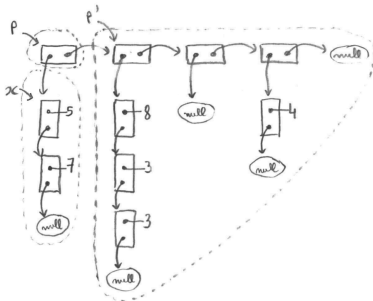
$$\{\lambda x. x \rightsquigarrow R X * p \rightsquigarrow \text{Mlistof } R(X :: L)\}$$

Specification of accesses: a brute force solution



$$\begin{aligned}
 p \rightsquigarrow \text{Mlistof } R L &= \exists K. \quad p \rightsquigarrow \text{MList } K \\
 &* \quad \bigotimes_{i \in \{0, \dots, |L| - 1\}} (K[i]) \rightsquigarrow R(L[i]) \\
 &* \quad \lceil |K| = |L| \rceil
 \end{aligned}$$

Specification of accesses: focus before read

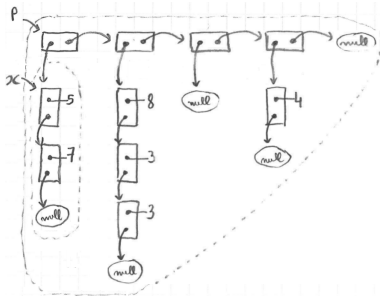


$$\begin{aligned}
 p \rightsquigarrow \text{Mlistof } R (X :: L) &= \exists x p'. \quad p \rightsquigarrow \{\text{hd}=x; \text{tl}=p'\} \\
 &\quad * \quad x \rightsquigarrow R X \\
 &\quad * \quad p' \rightsquigarrow \text{Mlistof } R L
 \end{aligned}$$

Then read using:

$$\{p \mapsto \{\text{hd}=x; \text{tl}=p'\}\} (p.\text{hd}) \{\lambda y. \text{'y = x'} * p \mapsto \{\text{hd}=x; \text{tl}=p'\}\}$$

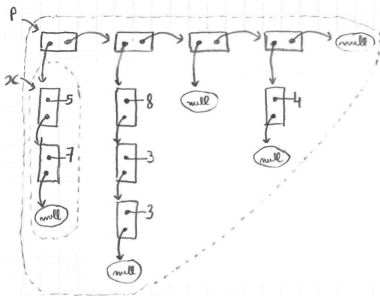
Specification of accesses with separating implication



Correct specification with $\multimap$:

$$\{p \rightsquigarrow \text{Mlistof } R(X :: L)\} (\text{head } p) \\ \{ \lambda x. x \rightsquigarrow R X * (x \rightsquigarrow R X \multimap p \rightsquigarrow \text{Mlistof } R(X :: L)) \}$$

Specification of accesses with separating implication



Correct specification with $\multimap$:

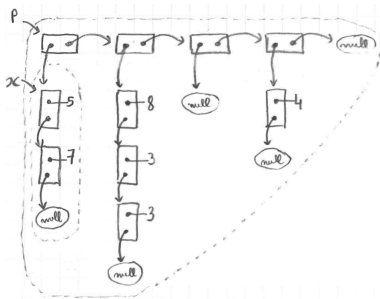
$$\{p \rightsquigarrow \text{Mlistof } R(X :: L)\} (\text{head } p)$$

$$\{\lambda x. x \rightsquigarrow R X * (x \rightsquigarrow R X \multimap p \rightsquigarrow \text{Mlistof } R(X :: L))\}$$

Exercise: What problem is there, e.g. if $x \rightsquigarrow R X$ is $x \mapsto X$ (i.e. $R = \text{Ref}$)?

Exercise: How to generalize the specification to solve this problem?

Specification of accesses with separating implication

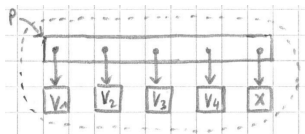
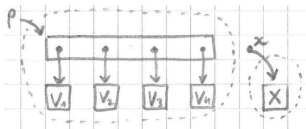


Correct specification with $\multimap$, generalized:

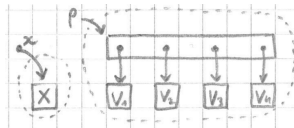
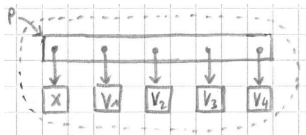
$$\{p \rightsquigarrow \text{Mlistof } R(X :: L)\} (\text{head } p) \\ \{\lambda x. x \rightsquigarrow R X * (\forall X', x \rightsquigarrow R X' \multimap p \rightsquigarrow \text{Mlistof } R(X' :: L))\}$$

Ownership transfer with a queue of mutable items

Push:



Pop:



Specification of queues of basic items

$$\{\text{true}\} (\text{create}()) \{\lambda p. p \rightsquigarrow \text{Queue nil}\}$$
$$\{p \rightsquigarrow \text{Queue } L\} (\text{push } x \text{ } p) \{\lambda_. p \rightsquigarrow \text{Queue } (L \& x)\}$$
$$\{p \rightsquigarrow \text{Queue } (x :: L)\} (\text{pop } p) \{\lambda r. \text{true} * p \rightsquigarrow \text{Queue } L\}$$
$$\{p \rightsquigarrow \text{Queue } L * p' \rightsquigarrow \text{Queue } L'\} (\text{concat } p \text{ } p') \{\lambda_. p \rightsquigarrow \text{Queue } (L \text{ ++ } L')\}$$

Specification of queues of mutable items

Exercise: specify functions over queues using a higher-order representation predicate written $p \rightsquigarrow \text{Queueof } R \ L$.

Shorthand: just write “Q R ” instead of “Queueof R ”.

Specification of queues of mutable items

Exercise: specify functions over queues using a higher-order representation predicate written $p \rightsquigarrow \text{Queueof } R \ L$.

Shorthand: just write “Q R ” instead of “Queueof R ”.

$\{\text{create}()\} \{\lambda p. p \rightsquigarrow \text{Queueof } R \text{ nil}\}$

Specification of queues of mutable items

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$\{\text{create}()\} \{\lambda p. p \rightsquigarrow \text{Queueof } R \text{ nil}\}$

$\{p \rightsquigarrow \text{Queueof } R L * x \rightsquigarrow R X\} (\text{push } x p) \{\lambda_. p \rightsquigarrow \text{Queueof } R (L \& X)\}$

Specification of queues of mutable items

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$$\{p \rightsquigarrow \text{Queueof } R (X :: L)\} (\text{pop } p) \{\lambda x. p \rightsquigarrow \text{Queueof } R L * x \rightsquigarrow R X\}$$

Specification of queues of mutable items

Exercise: specify functions over queues using a higher-order representation predicate written $p \rightsquigarrow \text{Queueof } R L$.

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$\{\text{[]}\} (\text{create}()) \{\lambda p. p \rightsquigarrow \text{Queueof } R \text{ nil}\}$

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$\{p \rightsquigarrow \text{Queueof } R (X :: L)\} (\text{pop } p) \{\lambda x. p \rightsquigarrow \text{Queueof } R L * x \rightsquigarrow R X\}$

$\{p \rightsquigarrow \text{Queueof } R L * p' \rightsquigarrow \text{Queueof } R L'\} (\text{concat } p p') \\ \{\lambda_. p \rightsquigarrow \text{Queueof } R (L \# L')\}$

The copy problem

Incorrect specification for copy:

$$\begin{aligned} &\{p \rightsquigarrow \text{Queueof } R\ L\} \\ &(\text{copy } p) \\ &\{\lambda p'. p \rightsquigarrow \text{Queueof } R\ L * p' \rightsquigarrow \text{Queueof } R\ L\} \end{aligned}$$

The copy problem

Incorrect specification for copy:

$$\begin{aligned} & \{p \rightsquigarrow \text{Queueof } R\ L\} \\ & (\text{copy } p) \\ & \{\lambda p'. p \rightsquigarrow \text{Queueof } R\ L * p' \rightsquigarrow \text{Queueof } R\ L\} \end{aligned}$$

Exercise: specify a function $\text{copy } f\ p$ that duplicates a mutable queue specified using Queueof , where f is a function to duplicate items.

The copy problem

Incorrect specification for copy:

$$\begin{aligned} & \{p \rightsquigarrow \text{Queueof } R L\} \\ & (\text{copy } p) \\ & \{\lambda p'. p \rightsquigarrow \text{Queueof } R L * p' \rightsquigarrow \text{Queueof } R L\} \end{aligned}$$

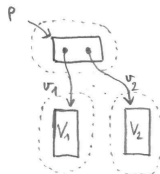
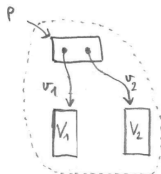
Exercise: specify a function $\text{copy } f p$ that duplicates a mutable queue specified using Queueof , where f is a function to duplicate items.

$$\begin{aligned} & (\forall x X. \{x \rightsquigarrow R X\} (f x) \{\lambda x'. x \rightsquigarrow R X * x' \rightsquigarrow R X\}) \\ \Rightarrow & \{p \rightsquigarrow \text{Queueof } R L\} \\ & (\text{copy } f p) \\ & \{\lambda p'. p \rightsquigarrow \text{Queueof } R L * p' \rightsquigarrow \text{Queueof } R L\} \end{aligned}$$

Chapter 20

Higher-order representation predicates for records

Representation for records



$$\begin{aligned}
 p \rightsquigarrow \text{MCellof } R_1 V_1 R_2 V_2 &\equiv \exists v_1 v_2. \quad p \rightsquigarrow \{\text{hd}=v_1; \text{tl}=v_2\} \\
 * \quad v_1 &\rightsquigarrow R_1 V_1 \\
 * \quad v_2 &\rightsquigarrow R_2 V_2
 \end{aligned}$$

Representation predicate for lists, revisited

$$\begin{aligned} p \rightsquigarrow \text{Mlistof } R L &\equiv \text{match } L \text{ with} \\ &\quad | \text{nil} \Rightarrow \text{'p = null'} \\ &\quad | X :: L' \Rightarrow \exists x p'. \quad p \rightsquigarrow \{\text{hd}=x; \text{tl}=p'\} \\ &\qquad \qquad \qquad * \quad x \rightsquigarrow R X \\ &\qquad \qquad \qquad * \quad p' \rightsquigarrow \text{Mlistof } R L' \\ p \rightsquigarrow \text{MCellof } R_1 V_1 R_2 V_2 &\equiv \exists v_1 v_2. \quad p \rightsquigarrow \{\text{hd}=v_1; \text{tl}=v_2\} \\ &\qquad \qquad \qquad * \quad v_1 \rightsquigarrow R_1 V_1 \\ &\qquad \qquad \qquad * \quad v_2 \rightsquigarrow R_2 V_2 \end{aligned}$$

Exercise: rewrite the specification of Mlistof using MCellof.

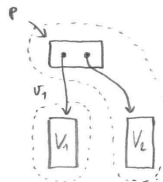
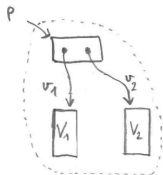
Representation predicate for lists, revisited

$$\begin{aligned} p \rightsquigarrow \text{Mlistof } R L &\equiv \text{match } L \text{ with} \\ &\quad | \text{nil} \Rightarrow \text{'}p = \text{null'} \\ &\quad | X :: L' \Rightarrow \exists x p'. \quad p \rightsquigarrow \{\text{hd}=x; \text{tl}=p'\} \\ &\qquad \qquad \qquad * \quad x \rightsquigarrow R X \\ &\qquad \qquad \qquad * \quad p' \rightsquigarrow \text{Mlistof } R L' \\ p \rightsquigarrow \text{MCellof } R_1 V_1 R_2 V_2 &\equiv \exists v_1 v_2. \quad p \rightsquigarrow \{\text{hd}=v_1; \text{tl}=v_2\} \\ &\qquad \qquad \qquad * \quad v_1 \rightsquigarrow R_1 V_1 \\ &\qquad \qquad \qquad * \quad v_2 \rightsquigarrow R_2 V_2 \end{aligned}$$

Exercise: rewrite the specification of Mlistof using MCellof.

$$\begin{aligned} p \rightsquigarrow \text{Mlistof } R L &\equiv \text{match } L \text{ with} \\ &\quad | \text{nil} \Rightarrow \text{'}p = \text{null'} \\ &\quad | X :: L' \Rightarrow p \rightsquigarrow \text{MCellof } R X (\text{Mlistof } R) L' \end{aligned}$$

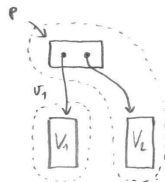
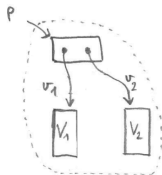
Focus/unfocus for accessing a record field



Focus on a field:

$$p \rightsquigarrow \text{MCellof } R_1 V_1 R_2 V_2 = \exists v_1. p \rightsquigarrow \text{MCellof Id } v_1 R_2 V_2 * v_1 \rightsquigarrow R_1 V_1$$

Focus/unfocus for accessing a record field



Focus on a field:

$$p \rightsquigarrow \text{MCellof } R_1 \ V_1 \ R_2 \ V_2 \quad = \quad \exists v_1. \ p \rightsquigarrow \text{MCellof Id } v_1 \ R_2 \ V_2 \ * \ v_1 \rightsquigarrow R_1 \ V_1$$

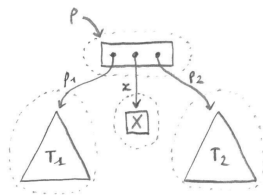
Access to a focused field:

$$\begin{aligned} &\{p \rightsquigarrow \text{MCellof Id } v_1 \ R_2 \ V_2\} \ (p.\text{hd}) \ \{\lambda x. \text{'}x = v_1\text{'} \ * \ p \rightsquigarrow \text{MCellof Id } v_1 \ R_2 \ V_2\} \\ &\{p \rightsquigarrow \text{MCellof Id } v_1 \ R_2 \ V_2\} \ (p.\text{hd} \leftarrow w) \ \{\lambda _. \ p \rightsquigarrow \text{MCellof Id } w \ R_2 \ V_2\} \end{aligned}$$

Chapter 21

Higher-order representation predicates for trees

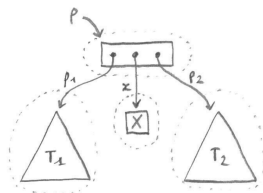
Binary tree: representation



$p \rightsquigarrow \text{Mtreeof } RT \equiv \text{match } T \text{ with}$

- | Leaf $\Rightarrow \text{'p = null'}$
- | Node $X T_1 T_2 \Rightarrow \exists x p_1 p_2.$
 - $p \mapsto \{\text{item}=x; \text{left}=p_1; \text{right}=p_2\}$
 - * $x \rightsquigarrow R X$
 - * $p_1 \rightsquigarrow \text{Mtreeof } R T_1$
 - * $p_2 \rightsquigarrow \text{Mtreeof } R T_2$

Binary tree: representation, revisited

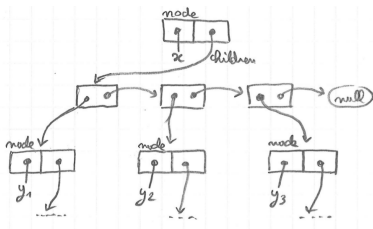


Representation predicate for tree cells:

$$\begin{aligned}
 p \rightsquigarrow \text{Nodeof } R_1 V_1 R_2 V_2 R_3 V_3 &\equiv \\
 \exists v_1 v_2 v_3. \quad p \mapsto \{ \text{item} = v_1; \text{left} = v_2; \text{right} = v_3 \} \\
 * v_1 \rightsquigarrow R_1 V_1 * v_2 \rightsquigarrow R_2 V_2 * v_3 \rightsquigarrow R_3 V_3
 \end{aligned}$$

$$\begin{aligned}
 p \rightsquigarrow \text{Mtreeof } R T &\equiv \text{match } T \text{ with} \\
 &\quad | \text{Leaf} \Rightarrow 'p = \text{null}' \\
 &\quad | \text{Node } X T_1 T_2 \Rightarrow \\
 &\quad \quad p \rightsquigarrow \text{Nodeof } R X (\text{Mtreeof } R) T_1 (\text{Mtreeof } R) T_2
 \end{aligned}$$

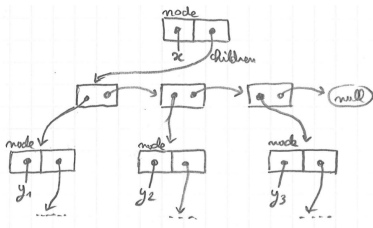
Trees with list of subtrees: implementation



```
type 'a node = {  
  mutable item : 'a;  
  mutable children : ('a node) cell }
```

```
Inductive tree (A:Type) : Type :=  
  | Leaf : tree A  
  | Node : A → list (tree A) → tree A.
```

Trees with list of subtrees: specification



$p \rightsquigarrow \text{Narytreeof } R T \equiv$

match T with

| Leaf $\Rightarrow$ ' $p = \text{null}$ '

| Node $X L \Rightarrow \exists x c. \quad p \mapsto \{\text{item}=x; \text{children}=c\}$

* $x \rightsquigarrow R X$

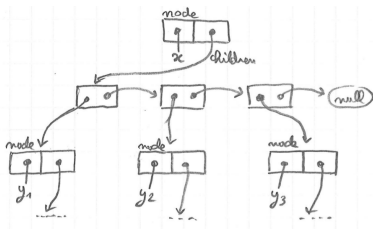
* $c \rightsquigarrow \text{Mlistof } (\text{Narytreeof } R) L$

Trees with list of subtrees: representation of nodes

$$\begin{aligned} p \rightsquigarrow \text{Nodeof } R_1 V_1 R_2 V_2 &\equiv \\ \exists v_1 v_2. \quad p \mapsto \{\text{item}=v_1; \text{children}=v_2\} & \\ * \quad v_1 \rightsquigarrow R_1 V_1 & \\ * \quad v_2 \rightsquigarrow R_2 V_2 & \end{aligned}$$

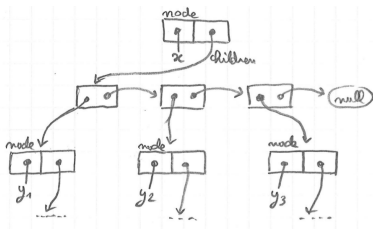
$$\begin{aligned} p \rightsquigarrow \text{Narytreeof } R T &\equiv \\ \text{match } T \text{ with} & \\ | \text{Leaf} \Rightarrow 'p = \text{null}' & \\ | \text{Node } X L \Rightarrow \exists xc. \quad p \mapsto \{\text{item}=x; \text{children}=c\} & \\ * \quad x \rightsquigarrow R X & \\ * \quad c \rightsquigarrow \text{Mlistof } (\text{Narytreeof } R) L & \end{aligned}$$

Trees with list of subtrees, revisited



Exercise: rewrite the specification of Narytreeof using Nodeof.

Trees with list of subtrees, revisited



Exercise: rewrite the specification of Narytreeof using Nodeof.

$p \rightsquigarrow \text{Narytreeof } RT \equiv$

match T with

| Leaf $\Rightarrow$ ' $p = \text{null}$ '

| Node $X L \Rightarrow p \rightsquigarrow \text{Nodeof } R X (\text{Mlistof } (\text{Narytreeof } R)) L$

Chapter 22

Iteration with higher-order representation predicates

Iteration on lists

Recall:

$$\begin{aligned} \forall f l I. \quad & (\forall x k. \{I\ k\} (f\ x) \{\lambda_. I\ (k\&x)\}) \\ \Rightarrow \quad & \{I\ \text{nil}\} (\text{iter}\ f\ l) \{\lambda_. I\ l\} \end{aligned}$$

$$\begin{aligned} \forall f p l I. \quad & (\forall x k. \{I\ k\} (f\ x) \{\lambda_. I\ (k\&x)\}) \\ \Rightarrow \quad & \{p \rightsquigarrow \text{MList } l * I\ \text{nil}\} (\text{miter}\ f\ p) \{\lambda_. p \rightsquigarrow \text{MList } l * I\ l\} \end{aligned}$$

Iteration on lists

Recall:

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$$\begin{aligned} & \forall f p l I. \quad (\forall x k. \{I\ k\} (f\ x) \{\lambda_. I\ (k\&x)\}) \\ & \Rightarrow \{p \rightsquigarrow \text{MList}\ l * I\ \text{nil}\} (\text{miter}\ f\ p) \{\lambda_. p \rightsquigarrow \text{MList}\ l * I\ l\} \end{aligned}$$

Challenge:

$$\begin{aligned} & (\forall x\ \dots \ \{\dots\} (f\ x) \{\lambda_. \dots\}) \\ & \Rightarrow \{p \rightsquigarrow \text{Mlistof}\ R\ L * \dots\} (\text{miter}\ f\ p) \{\lambda_. p \rightsquigarrow \dots * \dots\} \end{aligned}$$

Question: Can we use an invariant $I \equiv \lambda K. (\dots)$?

(i.e. with a spec of the form $\{p \rightsquigarrow \dots * I\ \text{nil}\} (\dots) \{p \rightsquigarrow \dots * I\ L\} \ ?$)

Iterating over a mutable list of mutable items

Exercise: specify the function `miter`, using an invariant of the form $J \ K \ K'$, describing the state before and the state after the iteration.

Iterating over a mutable list of mutable items

Exercise: specify the function `miter`, using an invariant of the form $J K K'$, describing the state before and the state after the iteration.

$$\begin{aligned} \forall f p R L J. \quad & \left(\forall x X K K'. \{x \rightsquigarrow R X * J K K'\} \right. \\ & (f x) \\ & \left. \{ \lambda _. \exists X'. x \rightsquigarrow R X' * J (K \& X) (K' \& X') \} \right) \\ \Rightarrow \quad & \{ p \rightsquigarrow \text{Mlistof } R L * J \text{ nil nil} \} \\ & (\text{miter } f p) \\ & \{ \lambda _. \exists L'. p \rightsquigarrow \text{Mlistof } R L' * J L L' \} \end{aligned}$$

Incrementing a mutable list of distinct references (1/2)

```
let incr_all p =  
  miter (fun x -> incr x) p  
  
let example_p =  
  { hd = ref 5; tl = { hd = ref 3; tl = null } }
```

$$x \rightsquigarrow \text{Ref } X \equiv x \mapsto X$$

Exercise: using the representation predicates `Ref` and `Mlistof`, specify the function `(fun x -> incr x)` and `incr_all`. What is $J \ K \ K'$?

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$$\{x \rightsquigarrow \text{Ref } X\} (\text{incr } x) \{\lambda_. x \rightsquigarrow \text{Ref } (X + 1)\}$$

Incrementing a mutable list of distinct references (1/2)

```
let incr_all p =  
  miter (fun x -> incr x) p  
  
let example_p =  
  { hd = ref 5; tl = { hd = ref 3; tl = null } }
```

$$x \rightsquigarrow \text{Ref } X \equiv x \mapsto X$$

Exercise: using the representation predicates `Ref` and `Mlistof`, specify the function `(fun x -> incr x)` and `incr_all`. What is $J \ K \ K'$?

$$\{x \rightsquigarrow \text{Ref } X\} (\text{incr } x) \{\lambda_. x \rightsquigarrow \text{Ref } (X + 1)\}$$

$$\{p \rightsquigarrow \text{Mlistof Ref } L\} (\text{incr_all } p) \{\lambda_. p \rightsquigarrow \text{Mlistof Ref } (\text{map } (+1) L)\}$$

Incrementing a mutable list of distinct references (2/2)

$$\begin{aligned}
 & \forall f p R L J. \quad \left(\forall x X K K'. \{x \rightsquigarrow R X * J K K'\} \right. \\
 & \qquad \qquad \qquad (f x) \\
 & \qquad \qquad \qquad \left. \{ \lambda _. \exists X'. x \rightsquigarrow R X' * J (K \& X) (K' \& X') \} \right) \\
 & \Rightarrow \{ p \rightsquigarrow \text{Mlistof } R L * J \text{ nil nil} \} \\
 & \quad (\text{miter } f p) \\
 & \quad \{ \lambda _. \exists L'. p \rightsquigarrow \text{Mlistof } R L' * J L L' \}
 \end{aligned}$$

Consider:

$$J K K' \equiv \text{'} K' = \text{map } (+1) K \text{'}$$

Derives:

$$\begin{aligned}
 & \left(\forall x X. \{x \rightsquigarrow \text{Ref } X\} (\text{fun } x \rightarrow \text{incr } x) x \{ \lambda _. x \rightsquigarrow \text{Ref } (X + 1) \} \right) \Rightarrow \\
 & \{ p \rightsquigarrow \text{Mlistof Ref } L \} (\text{incr_all } p) \{ \lambda _. p \rightsquigarrow \text{Mlistof Ref } (\text{map } (+1) L) \}
 \end{aligned}$$

Chapter 23

Resource analysis in Separation Logic

Controlling deallocation

(1) Remove the garbage collection rule:

$$\frac{\{H\} t \{Q \star \text{GC}\}}{\{H\} t \{Q\}} \text{GC-POST}$$

(2) Add a “free” function for explicit deallocation:

$$\{r \mapsto v\} (\text{free } r) \{ \lambda _ . \text{''} \}$$

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(3) Theorem: for a full program execution starting in the empty heap, all the data still allocated at the end is described in the post-condition.

(4) Corollary: terminating on the empty heap ensures no memory leaks.

$$\{ \text{''} \} t \{ \lambda n. \text{' } P \text{ } n \text{' } \}$$

Controlling deallocation

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(4) Corollary: terminating on the empty heap ensures no memory leaks.

$$\{\text{''}\} t \{\lambda n. \text{'P } n'\}$$

(note: a separation logic with GC can be called “affine” or “intuitionistic”)

File handle protocols

Goal: ensure that if a file is open then it is eventually closed.

$$f \rightsquigarrow \text{File } L$$

where $(f : \text{loc})$ denotes the file handler,
and $(L : \text{list char})$ denotes the remaining bytes to read.

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$$f \rightsquigarrow \text{File } L$$

where $(f : \text{loc})$ denotes the file handler,
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$$\{\text{''}\} \text{ (fopen s) } \{\lambda f. \exists L. f \rightsquigarrow \text{File } L\}$$

File handle protocols

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$$f \rightsquigarrow \text{File } L$$

where $(f : \text{loc})$ denotes the file handler,
and $(L : \text{list char})$ denotes the remaining bytes to read.

$$\begin{aligned} \{ \text{''} \} \text{ (fopen s) } & \{ \lambda f. \exists L. f \rightsquigarrow \text{File } L \} \\ \{ f \rightsquigarrow \text{File } (c :: L) \} \text{ (fread f) } & \{ \lambda x. \text{''}x = c\text{' * } f \rightsquigarrow \text{File } L \} \end{aligned}$$

File handle protocols

Goal: ensure that if a file is open then it is eventually closed.

$$f \rightsquigarrow \text{File } L$$

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and $(L : \text{list char})$ denotes the remaining bytes to read.

$$\begin{aligned} & \{\text{' '}\} \text{ (fopen s) } \{\lambda f. \exists L. f \rightsquigarrow \text{File } L\} \\ & \{f \rightsquigarrow \text{File } (c :: L)\} \text{ (fread f) } \{\lambda x. \text{' } x = c \text{' } * f \rightsquigarrow \text{File } L\} \\ & \{f \rightsquigarrow \text{File } L\} \text{ (fclose f) } \{\lambda _. \text{' '}\} \end{aligned}$$

Complexity analysis

Time credits:

$$\$x : \text{Hprop} \quad \text{where } x \in \mathbb{R}^+$$

Properties:

$$\$(x + y) = \$x * \$y \quad \text{and} \quad \$0 = \text{''}$$

Complexity analysis

Time credits:

$$\$x : \text{Hprop} \quad \text{where } x \in \mathbb{R}^+$$

Properties:

$$\$(x + y) = \$x * \$y \quad \text{and} \quad \$0 = \text{''}$$

Principle:

The execution of every instruction costs \$1.

Simplification:

Entering the body of a function or a loop costs \$1.

Time credits in pre-conditions

Constant time:

$$\{t \rightsquigarrow \text{Array } M * \$c\} (\text{Array.length } t) \{ \lambda n. \ulcorner n = |M| \urcorner * t \rightsquigarrow \text{Array } M \}$$

Time credits in pre-conditions

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Linear time:

$$\{ \$ (c_1 n + c_2) \} (\text{Array.make } n \ v) \{ \lambda t. \exists L. t \rightsquigarrow \text{Array } L * \ulcorner \dots \urcorner \}$$

Time credits in pre-conditions

Constant time:

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Linear time:

$$\{ \$(c_1 n + c_2) \} (\text{Array.make } n \ v) \{ \lambda t. \exists L. t \rightsquigarrow \text{Array } L * \ulcorner \dots \urcorner \}$$

Quasilinear time:

$$\begin{aligned} & \{t \rightsquigarrow \text{Array } L * \$(c_1 |L| \log |L| + c_2)\} \\ & (\text{Array.sort } t) \\ & \{ \lambda t. \exists L'. t \rightsquigarrow \text{Array } L' * \ulcorner \dots \urcorner \} \end{aligned}$$

Amortized analysis

Stack of unbounded size with amortized constant-time operations:

$$\begin{aligned} \{\$c\} & \quad (\text{Stack.create}()) \{ \lambda_. s \rightsquigarrow \text{Stack nil} \} \\ \{s \rightsquigarrow \text{Stack } L * \$c\} & \quad (\text{Stack.push } s \ x) \{ \lambda_. s \rightsquigarrow \text{Stack } (x :: L) \} \\ \{s \rightsquigarrow \text{Stack } (x :: L) * \$c\} & \quad (\text{Stack.pop } s) \quad \{ \lambda y. \text{'}y = x\text{'} * s \rightsquigarrow \text{Stack } L \} \end{aligned}$$

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$$\begin{aligned} \{\$c\} & \quad (\text{Stack.create}()) \{ \lambda_. s \rightsquigarrow \text{Stack nil} \} \\ \{s \rightsquigarrow \text{Stack } L * \$c\} & \quad (\text{Stack.push } s \ x) \{ \lambda_. s \rightsquigarrow \text{Stack } (x :: L) \} \\ \{s \rightsquigarrow \text{Stack } (x :: L) * \$c\} & \quad (\text{Stack.pop } s) \quad \{ \lambda y. \text{'}y = x\text{'} * s \rightsquigarrow \text{Stack } L \} \end{aligned}$$

Representation predicate with a potential function:

$$\begin{aligned} s \rightsquigarrow \text{Stack } L \quad \equiv \quad \exists ntMk. \quad & s \mapsto \{\text{size}=n; \text{data}=t\} \\ & * \quad t \rightsquigarrow \text{Array } M \\ & * \quad \text{'}n = |L| \leq |M| = 2^k\text{'} \\ & * \quad \text{'}\forall i \in [0, n). \ M[i] = L[i]\text{'} \\ & * \quad \$(c' \cdot \text{abs}(n - |M|/2)) \end{aligned}$$

Space complexity analysis

Suppose $\diamond 1$ represents one *space credit* (Hoffmann, 1999)

Then allocation consumes credits; deallocation produces credits :

$$\begin{array}{lcl} \{\diamond \text{size}(b)\} & \text{alloc}(b) & \{\lambda x. x \mapsto b\} \\ \{x \mapsto b\} & \text{free}(x) & \{\lambda_. \diamond \text{size}(b)\} \end{array}$$

The same mechanisms apply (e.g. $\diamond(a + b) = \diamond a * \diamond b$, amortized analysis, etc.).

Space complexity analysis

Suppose $\diamond 1$ represents one *space credit* (Hoffmann, 1999)

Then allocation consumes credits; deallocation produces credits :

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The same mechanisms apply (e.g. $\diamond(a + b) = \diamond a * \diamond b$, amortized analysis, etc.).

What if we add **garbage collection** ?

Fractional permissions

$$(r \overset{\alpha}{\mapsto} v) \quad \text{with } 0 < \alpha \leq 1$$

Splitting and merging:

$$(r \mapsto v) = (r \overset{1}{\mapsto} v) = (r \overset{1/2}{\mapsto} v) * (r \overset{1/2}{\mapsto} v)$$

More generally, if $0 < \alpha, \beta \leq 1$:

$$(r \overset{\alpha+\beta}{\mapsto} v) = (r \overset{\alpha}{\mapsto} v) * (r \overset{\beta}{\mapsto} v)$$

Fractional permissions

$$(r \mapsto^\alpha v) \quad \text{with } 0 < \alpha \leq 1$$

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Values must agree: if $0 < \alpha, \beta \leq 1$:

$$\left((r \mapsto^\alpha v) * (r \mapsto^\beta w) \right) \triangleright \left((r \mapsto^\alpha v) * (r \mapsto^\beta w) * \ulcorner v = w \urcorner \right)$$

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$$\left((r \mapsto^\alpha v) * (r \mapsto^\beta w) \right) \triangleright \left((r \mapsto^\alpha v) * (r \mapsto^\beta w) * \ulcorner v = w \urcorner \right)$$

Operations:

$$\begin{aligned} & \{ \ulcorner \cdot \urcorner \} \text{ (ref } v) \{ \lambda r. r \mapsto^1 v \} \\ & \{ r \mapsto^1 v' \} (r := v) \{ \lambda _. r \mapsto^1 v \} \\ \forall \alpha. & \quad \{ r \mapsto^\alpha v \} \text{ (!} r \text{)} \{ \lambda x. \ulcorner x = v \urcorner * (r \mapsto^\alpha v) \} \end{aligned}$$

Fractional permissions in practice

$$\begin{aligned} & \forall \alpha \beta. \{a_1 \overset{\alpha}{\rightsquigarrow} \text{Array } L_1 * a_2 \overset{\beta}{\rightsquigarrow} \text{Array } L_2\} \\ & \quad (\text{concat } a_1 \ a_2) \\ & \quad \{\lambda a_3. a_1 \overset{\alpha}{\rightsquigarrow} \text{Array } L_1 * a_2 \overset{\beta}{\rightsquigarrow} \text{Array } L_2 * a_3 \overset{1}{\rightsquigarrow} \text{Array } (L_1 \text{ ++ } L_2)\} \end{aligned}$$

Fractional permissions in practice

$$\begin{aligned} \forall \alpha \beta. \{ & a_1 \overset{\alpha}{\rightsquigarrow} \text{Array } L_1 * a_2 \overset{\beta}{\rightsquigarrow} \text{Array } L_2 \} \\ & (\text{concat } a_1 \ a_2) \\ \{ & \lambda a_3. a_1 \overset{\alpha}{\rightsquigarrow} \text{Array } L_1 * a_2 \overset{\beta}{\rightsquigarrow} \text{Array } L_2 * a_3 \overset{1}{\rightsquigarrow} \text{Array } (L_1 \uparrow\uparrow L_2) \} \end{aligned}$$

Limitations:

- need to quantify fractions explicitly,
- need to syntactic sugar to avoid copy-pasting,
- need to re-establish post-conditions,
- a fraction $\frac{1}{2}H$ cannot be defined for arbitrary H .

Those can be alleviated with a duplicable read-only modality $\text{RO}(H)$.

Chapter 24

Parallelism and Concurrency

Parallel pairs

A parallel pair, written $(|t_1, t_2|)$, for evaluating two subterms in parallel.
(Note: one often sees $t_1 || t_2$ for $\text{let } ((), ()) = (|t_1, t_2|) \text{ in } ()$.)

Computing: $a[i] + a[i + 1] + \dots + a[j - 1]$.

```
let rec sum a i j =  
  if j - i = 1 then a.(i) else begin  
    let m = (i+j) / 2 in  
    let (s1,s2) = (| sum a i m, sum a m j |) in  
    s1 + s2  
  end
```

Efficient use of parallel pairs with granularity control

```
let rec sum a i j =  
  if j - i < sequential_cutoff then begin  
    let r = ref 0 in  
    for k = i to j-1 do  
      r := !r + a.(k)  
    done;  
    !r  
  end else begin  
    let m = (i+j) / 2 in  
    let (s1,s2) = (| sum a i m, sum a m j |) in  
      s1 + s2  
  end
```

Efficient use of parallel pairs with granularity control

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let rec sum a i j =  
  if j - i < sequential_cutoff then begin  
    let r = ref 0 in  
    for k = i to j-1 do  
      r := !r + a.(k)  
    done;  
    !r  
  end else begin  
    let m = (i+j) / 2 in  
    let (s1,s2) = (| sum a i m, sum a m j |) in  
      s1 + s2  
  end
```

Generalizable to map-reduce: $f(t[0]) \oplus f(a[1]) \oplus \dots \oplus f(a[n-1])$.
(on which condition on $\oplus$?)

Reasoning rule for parallel pairs

$$\frac{\{H_1\} t_1 \{Q_1\} \quad \{H_2\} t_2 \{Q_2\}}{\{H_1 * H_2\} (|t_1, t_2|) \{Q_1 \star Q_2\}} \text{ PARALLEL}$$

where $Q_1 \star Q_2 \equiv \lambda(x_1, x_2). Q_1 x_1 * Q_2 x_2$

Reasoning rule for parallel pairs

$$\frac{\{H_1\} t_1 \{Q_1\} \quad \{H_2\} t_2 \{Q_2\}}{\{H_1 * H_2\} (|t_1, t_2|) \{Q_1 \star Q_2\}} \text{ PARALLEL}$$

where $Q_1 \star Q_2 \equiv \lambda(x_1, x_2). Q_1 x_1 * Q_2 x_2$

This rule restricts parallel threads to act on disjoint parts of memory.
(No need for non-interference conditions.)

Concurrent locks: example

```
let r = ref 0
let s = ref n
let p = create_lock()

let concurrent_step () =
  acquire_lock p;
  incr r;
  decr s;
  release_lock p
```

Concurrent locks: example

```
let r = ref 0
let s = ref n
let p = create_lock()

let concurrent_step () =
  acquire_lock p;
  incr r;
  decr s;
  release_lock p
```

Heap predicate $p \rightsquigarrow \text{Lock } H$ asserts that lock p protects an invariant H .
Here:

$$p \rightsquigarrow \text{Lock } (\exists i. (r \mapsto i) * (s \mapsto n - i))$$

Concurrent locks: specification of operations

Duplicable representation predicate:

$$p \rightsquigarrow \text{Lock } H$$

Operations:

$$\forall H. \quad \{H\} (\text{create_lock } ()) \{ \lambda p. p \rightsquigarrow \text{Lock } H \}$$

$$\forall p H. \quad \{p \rightsquigarrow \text{Lock } H\} (\text{acquire_lock } p) \{ \lambda -. H * p \rightsquigarrow \text{Lock } H \}$$

$$\forall p H. \quad \{H * p \rightsquigarrow \text{Lock } H\} (\text{release_lock } p) \{ \lambda -. p \rightsquigarrow \text{Lock } H \}$$

Concurrent locks: exercise

$$\forall H. \quad \{H\} (\text{create_lock } ()) \{ \lambda p. p \rightsquigarrow \text{Lock } H \}$$
$$\forall p H. \quad \{p \rightsquigarrow \text{Lock } H\} (\text{acquire_lock } p) \{ \lambda _. H * p \rightsquigarrow \text{Lock } H \}$$
$$\forall p H. \{H * p \rightsquigarrow \text{Lock } H\} (\text{release_lock } p) \{ \lambda _. p \rightsquigarrow \text{Lock } H \}$$

Exercise: Describe the state before each instruction (except line 5).
Explicit the instantiation of the existential in the invariant.

```
1  let r = ref 0
2  let s = ref n
3  let p = create_lock ()
4
5  let concurrent_step () =
6    acquire_lock p;
7    incr r;
8    decr s;
9    release_lock p
```

Concurrent locks: exercise

Exercise: Describe the state before each instruction (except line 5).
Explicit the instantiation of the existential in the invariant.

```
1  let r = ref 0
2  let s = ref n
3  let p = create_lock ()
4
5  let concurrent_step () =
6      acquire_lock p;
7      incr r;
8      decr s;
9      release_lock p
```

1: $\top$. 2: $r \mapsto 0$. 3: $r \mapsto 0 * s \mapsto n$.

4: $p \rightsquigarrow \text{Lock}(\exists i. (r \mapsto i) * (s \mapsto n - i))$.

7: $(r \mapsto i) * (s \mapsto n - i)$. 8: $(r \mapsto i + 1) * (s \mapsto n - i)$.

9: $(r \mapsto i + 1) * (s \mapsto n - i - 1)$. Instantiate the invariant with $i + 1$.

Concurrent locks: non-example

```
let r = ref 0
let p = create_lock()

let f () =
  acquire_lock p;
  incr r;
  release_lock p

let () =
  let _ = (| f(), f() |) in
  acquire_lock p;
  assert (!r == 2)
```

Chapter 25

Ghost state

Same non-example

```
let r = ref 0
let p = create_lock()
```

acquire_lock p;		acquire_lock p;
r := !r + 1;		r := !r + 1;
release_lock p;		release_lock p;

```
acquire_lock p;
assert (!r == 2);
```

Same non-example

```
let r = ref 0
let p = create_lock()
```

acquire_lock p;		acquire_lock p;
r := !r + 1;		r := !r + 1;
release_lock p;		release_lock p;

```
acquire_lock p;
assert (!r == 2);
```

$$p \rightsquigarrow \text{Lock}(\exists n. r \mapsto n * \text{r} \dots ? \dots \text{r})$$

Same non-example

```
let r = ref 0
let p = create_lock()
```

acquire_lock p;		acquire_lock p;
r := !r + 1;		r := !r + 1;
release_lock p;		release_lock p;

```
acquire_lock p;
assert (!r == 2);
```

$$p \rightsquigarrow \text{Lock}(\exists n. r \mapsto n * \ulcorner \dots ? \dots \urcorner)$$

Problem: it is impossible to prove, only with invariants, that this program does not crash (i.e. to prove $\{\text{True}\} \text{ program } \{\text{True}\}$)

More variables! Ghost variables.

```
let r = ref 0
let r1 = ref 0
let r2 = ref 0
let p = create_lock()
```

acquire_lock p;		acquire_lock p;
r := !r + 1;		r := !r + 1;
r1 := !r1 + 1;		r2 := !r2 + 1;
release_lock p;		release_lock p;

```
acquire_lock p;
assert (!r == 2);
```

More variables! Ghost variables.

```
let r = ref 0
let r1 = ref 0
let r2 = ref 0
let p = create_lock()
```

acquire_lock p;		acquire_lock p;
r := !r + 1;		r := !r + 1;
r1 := !r1 + 1;		r2 := !r2 + 1;
release_lock p;		release_lock p;

```
acquire_lock p;
assert (!r == 2);
```

Exercise: Give a lock invariant that allows proving $\{\text{True}\}$ program $\{\text{True}\}$ (hint: fractional permissions). Then prove the triple.

More variables! Ghost variables.

```
let r = ref 0
let r1 = ref 0
let r2 = ref 0
let p = create_lock()
```

acquire_lock p;		acquire_lock p;
r := !r + 1;		r := !r + 1;
r1 := !r1 + 1;		r2 := !r2 + 1;
release_lock p;		release_lock p;

```
acquire_lock p;
assert (!r == 2);
```

Exercise: Give a lock invariant that allows proving $\{\text{True}\}$ program $\{\text{True}\}$ (hint: fractional permissions). Then prove the triple.

$$p \rightsquigarrow \text{Lock} (\exists n, n_1, n_2. r \mapsto n * r_1 \xrightarrow{1/2} n_1 * r_2 \xrightarrow{1/2} n_2 * \ulcorner n = n_1 + n_2 \urcorner)$$

Proof

$$H \equiv \exists n, n_1, n_2. r \mapsto n * r_1 \stackrel{1/2}{\mapsto} n_1 * r_2 \stackrel{1/2}{\mapsto} n_2 * \ulcorner n = n_1 + n_2 \urcorner$$

```
let r = ref 0
```

```
let r1 = ref 0
```

```
let r2 = ref 0
```

```
{r ↦ 0 * r1 ↦ 0 * r2 ↦ 0}
```

Proof

$$H \equiv \exists n, n_1, n_2. r \mapsto n * r_1 \xrightarrow{1/2} n_1 * r_2 \xrightarrow{1/2} n_2 * \text{'}n = n_1 + n_2\text{'}$$

`let r = ref 0`

`let r1 = ref 0`

`let r2 = ref 0`

$\{r \mapsto 0 * r_1 \mapsto 0 * r_2 \mapsto 0\}$

$\{H * r_1 \xrightarrow{1/2} 0 * r_2 \xrightarrow{1/2} 0\}$

Proof

$$H \equiv \exists n, n_1, n_2. r \mapsto n * r_1 \xrightarrow{1/2} n_1 * r_2 \xrightarrow{1/2} n_2 * \ulcorner n = n_1 + n_2 \urcorner$$

```
let r = ref 0
let r1 = ref 0
let r2 = ref 0
{r ↦ 0 * r1 ↦ 0 * r2 ↦ 0}
{H * r1  $\xrightarrow{1/2}$  0 * r2  $\xrightarrow{1/2}$  0}
let p = create_lock()
```

Proof

$$H \equiv \exists n, n_1, n_2. r \mapsto n * r_1 \xrightarrow{1/2} n_1 * r_2 \xrightarrow{1/2} n_2 * \ulcorner n = n_1 + n_2 \urcorner$$

```
let r = ref 0
```

```
let r1 = ref 0
```

```
let r2 = ref 0
```

```
{r ↦ 0 * r1 ↦ 0 * r2 ↦ 0}
```

```
{H * r1  $\xrightarrow{1/2}$  0 * r2  $\xrightarrow{1/2}$  0}
```

```
let p = create_lock()
```

```
{p  $\rightsquigarrow$  Lock H * r1  $\xrightarrow{1/2}$  0 * r2  $\xrightarrow{1/2}$  0}
```

Proof

$$H \equiv \exists n, n_1, n_2. r \mapsto n * r_1 \xrightarrow{1/2} n_1 * r_2 \xrightarrow{1/2} n_2 * \lceil n = n_1 + n_2 \rceil$$

```
let r = ref 0
```

```
let r1 = ref 0
```

```
let r2 = ref 0
```

```
{r ↦ 0 * r1 ↦ 0 * r2 ↦ 0}
```

```
{H * r1  $\xrightarrow{1/2}$  0 * r2  $\xrightarrow{1/2}$  0}
```

```
let p = create_lock()
```

```
{p  $\rightsquigarrow$  Lock H * r1  $\xrightarrow{1/2}$  0 * r2  $\xrightarrow{1/2}$  0}
```

```
{(p  $\rightsquigarrow$  Lock H * r1  $\xrightarrow{1/2}$  0) * (p  $\rightsquigarrow$  Lock H * r2  $\xrightarrow{1/2}$  0)}
```

Proof

$$H \equiv \exists n, n_1, n_2. r \mapsto n * r_1 \xrightarrow{1/2} n_1 * r_2 \xrightarrow{1/2} n_2 * \lceil n = n_1 + n_2 \rceil$$

```
let r = ref 0
let r1 = ref 0
let r2 = ref 0
{r ↦ 0 * r1 ↦ 0 * r2 ↦ 0}
{H * r1  $\xrightarrow{1/2}$  0 * r2  $\xrightarrow{1/2}$  0}
let p = create_lock()
{p  $\rightsquigarrow$  Lock H * r1  $\xrightarrow{1/2}$  0 * r2  $\xrightarrow{1/2}$  0}
{(p  $\rightsquigarrow$  Lock H * r1  $\xrightarrow{1/2}$  0) * (p  $\rightsquigarrow$  Lock H * r2  $\xrightarrow{1/2}$  0)}
{p  $\rightsquigarrow$  Lock H * r1  $\xrightarrow{1/2}$  0} || {p  $\rightsquigarrow$  Lock H * r2  $\xrightarrow{1/2}$  0}
acquire_lock p;          acquire_lock p;
r := !r + 1;             r := !r + 1;
...                      ...
```

Left thread

$$H \equiv \exists n, n_1, n_2. r \mapsto n * r_1 \xrightarrow{1/2} n_1 * r_2 \xrightarrow{1/2} n_2 * \text{'}n = n_1 + n_2\text{'}$$

$$\{p \rightsquigarrow \text{Lock } H * r_1 \xrightarrow{1/2} 0\}$$

acquire_lock p;

Left thread

$$H \equiv \exists n, n_1, n_2. r \mapsto n * r_1 \xrightarrow{1/2} n_1 * r_2 \xrightarrow{1/2} n_2 * \text{'}n = n_1 + n_2\text{'}$$

$$\{p \rightsquigarrow \text{Lock } H * r_1 \xrightarrow{1/2} 0\}$$

acquire_lock p;

$$\{p \rightsquigarrow \text{Lock } H * r_1 \xrightarrow{1/2} 0 * H\}$$

Left thread

$$H \equiv \exists n, n_1, n_2. r \mapsto n * r_1 \xrightarrow{1/2} n_1 * r_2 \xrightarrow{1/2} n_2 * \ulcorner n = n_1 + n_2 \urcorner$$

$$\{p \rightsquigarrow \text{Lock } H * r_1 \xrightarrow{1/2} 0\}$$

acquire_lock p;

$$\{p \rightsquigarrow \text{Lock } H * r_1 \xrightarrow{1/2} 0 * H\} \text{ so, for some } n, n_1, n_2 \text{ s. that } n = n_1 + n_2:$$

$$\{p \rightsquigarrow \text{Lock } H * r_1 \xrightarrow{1/2} 0 * r \mapsto n * r_1 \xrightarrow{1/2} n_1 * r_2 \xrightarrow{1/2} n_2\}$$

r := !r + 1;

Left thread

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$$\{p \rightsquigarrow \text{Lock } H * r_1 \xrightarrow{1/2} 0 * r \mapsto n * r_1 \xrightarrow{1/2} n_1 * r_2 \xrightarrow{1/2} n_2\}$$

r := !r + 1;

$$\{p \rightsquigarrow \text{Lock } H * r_1 \xrightarrow{1/2} 0 * r \mapsto n + 1 * r_1 \xrightarrow{1/2} n_1 * r_2 \xrightarrow{1/2} n_2\}$$

Left thread

$$H \equiv \exists n, n_1, n_2. r \mapsto n * r_1 \xrightarrow{1/2} n_1 * r_2 \xrightarrow{1/2} n_2 * \text{'}n = n_1 + n_2\text{'}$$

$$\{p \rightsquigarrow \text{Lock } H * r_1 \xrightarrow{1/2} 0\}$$

acquire_lock p;

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$$\{p \rightsquigarrow \text{Lock } H * r_1 \xrightarrow{1} 0 * r \mapsto n + 1 * r_2 \xrightarrow{1/2} n_2\}$$

r1 := !r1 + 1;

Left thread

$$H \equiv \exists n, n_1, n_2. r \mapsto n * r_1 \xrightarrow{1/2} n_1 * r_2 \xrightarrow{1/2} n_2 * \text{'}n = n_1 + n_2\text{'}$$

$$\{p \rightsquigarrow \text{Lock } H * r_1 \xrightarrow{1/2} 0\}$$

acquire_lock p;

$$\{p \rightsquigarrow \text{Lock } H * r_1 \xrightarrow{1/2} 0 * H\} \text{ so, for some } n, n_1, n_2 \text{ s. that } n = n_1 + n_2:$$

$$\{p \rightsquigarrow \text{Lock } H * r_1 \xrightarrow{1/2} 0 * r \mapsto n * r_1 \xrightarrow{1/2} n_1 * r_2 \xrightarrow{1/2} n_2\}$$

r := !r + 1;

$$\{p \rightsquigarrow \text{Lock } H * r_1 \xrightarrow{1/2} 0 * r \mapsto n + 1 * r_1 \xrightarrow{1/2} n_1 * r_2 \xrightarrow{1/2} n_2\}$$

$$\{p \rightsquigarrow \text{Lock } H * r_1 \xrightarrow{1} 0 * r \mapsto n + 1 * r_2 \xrightarrow{1/2} n_2\}$$

r1 := !r1 + 1;

$$\{p \rightsquigarrow \text{Lock } H * r_1 \xrightarrow{1} 1 * r \mapsto n + 1 * r_2 \xrightarrow{1/2} n_2\}$$

Left thread

$$H \equiv \exists n, n_1, n_2. r \mapsto n * r_1 \xrightarrow{1/2} n_1 * r_2 \xrightarrow{1/2} n_2 * \text{'}n = n_1 + n_2\text{'}$$

$$\{p \rightsquigarrow \text{Lock } H * r_1 \xrightarrow{1/2} 0\}$$

acquire_lock p;

$$\{p \rightsquigarrow \text{Lock } H * r_1 \xrightarrow{1/2} 0 * H\} \text{ so, for some } n, n_1, n_2 \text{ s. that } n = n_1 + n_2:$$

$$\{p \rightsquigarrow \text{Lock } H * r_1 \xrightarrow{1/2} 0 * r \mapsto n * r_1 \xrightarrow{1/2} n_1 * r_2 \xrightarrow{1/2} n_2\}$$

r := !r + 1;

$$\{p \rightsquigarrow \text{Lock } H * r_1 \xrightarrow{1/2} 0 * r \mapsto n + 1 * r_1 \xrightarrow{1/2} n_1 * r_2 \xrightarrow{1/2} n_2\}$$

$$\{p \rightsquigarrow \text{Lock } H * r_1 \xrightarrow{1} 0 * r \mapsto n + 1 * r_2 \xrightarrow{1/2} n_2\}$$

r1 := !r1 + 1;

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$$\{p \rightsquigarrow \text{Lock } H * r_1 \xrightarrow{1/2} 1 * r \mapsto n + 1 * r_1 \xrightarrow{1/2} 1 * r_2 \xrightarrow{1/2} n_2\}$$

Left thread

$$H \equiv \exists n, n_1, n_2. r \mapsto n * r_1 \xrightarrow{1/2} n_1 * r_2 \xrightarrow{1/2} n_2 * \text{'}n = n_1 + n_2\text{'}$$

$$\{p \rightsquigarrow \text{Lock } H * r_1 \xrightarrow{1/2} 0\}$$

acquire_lock p;

$$\{p \rightsquigarrow \text{Lock } H * r_1 \xrightarrow{1/2} 0 * H\} \text{ so, for some } n, n_1, n_2 \text{ s. that } n = n_1 + n_2:$$

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r := !r + 1;

$$\{p \rightsquigarrow \text{Lock } H * r_1 \xrightarrow{1/2} 0 * r \mapsto n + 1 * r_1 \xrightarrow{1/2} n_1 * r_2 \xrightarrow{1/2} n_2\}$$

$$\{p \rightsquigarrow \text{Lock } H * r_1 \xrightarrow{1} 0 * r \mapsto n + 1 * r_2 \xrightarrow{1/2} n_2\}$$

r1 := !r1 + 1;

$$\{p \rightsquigarrow \text{Lock } H * r_1 \xrightarrow{1} 1 * r \mapsto n + 1 * r_2 \xrightarrow{1/2} n_2\}$$

$$\{p \rightsquigarrow \text{Lock } H * r_1 \xrightarrow{1/2} 1 * r \mapsto n + 1 * r_1 \xrightarrow{1/2} 1 * r_2 \xrightarrow{1/2} n_2\}$$

$$\{p \rightsquigarrow \text{Lock } H * r_1 \xrightarrow{1/2} 1 * H\}$$

release_lock p;

Left thread

$$H \equiv \exists n, n_1, n_2. r \mapsto n * r_1 \xrightarrow{1/2} n_1 * r_2 \xrightarrow{1/2} n_2 * \ulcorner n = n_1 + n_2 \urcorner$$

$$\{p \rightsquigarrow \text{Lock } H * r_1 \xrightarrow{1/2} 0\}$$

acquire_lock p;

$$\{p \rightsquigarrow \text{Lock } H * r_1 \xrightarrow{1/2} 0 * H\} \text{ so, for some } n, n_1, n_2 \text{ s. that } n = n_1 + n_2:$$

$$\{p \rightsquigarrow \text{Lock } H * r_1 \xrightarrow{1/2} 0 * r \mapsto n * r_1 \xrightarrow{1/2} n_1 * r_2 \xrightarrow{1/2} n_2\}$$

r := !r + 1;

$$\{p \rightsquigarrow \text{Lock } H * r_1 \xrightarrow{1/2} 0 * r \mapsto n + 1 * r_1 \xrightarrow{1/2} n_1 * r_2 \xrightarrow{1/2} n_2\}$$

$$\{p \rightsquigarrow \text{Lock } H * r_1 \xrightarrow{1} 0 * r \mapsto n + 1 * r_2 \xrightarrow{1/2} n_2\}$$

r1 := !r1 + 1;

$$\{p \rightsquigarrow \text{Lock } H * r_1 \xrightarrow{1} 1 * r \mapsto n + 1 * r_2 \xrightarrow{1/2} n_2\}$$

$$\{p \rightsquigarrow \text{Lock } H * r_1 \xrightarrow{1/2} 1 * r \mapsto n + 1 * r_1 \xrightarrow{1/2} 1 * r_2 \xrightarrow{1/2} n_2\}$$

$$\{p \rightsquigarrow \text{Lock } H * r_1 \xrightarrow{1/2} 1 * H\}$$

release_lock p;

$$\{p \rightsquigarrow \text{Lock } H * r_1 \xrightarrow{1/2} 1\}$$

Right thread

$$H \equiv \exists n, n_1, n_2. r \mapsto n * (r_1 \xrightarrow{1/2} n_1) * (r_2 \xrightarrow{1/2} n_2) * \text{'}n = n_1 + n_2\text{'}$$

$$\{p \rightsquigarrow \text{Lock } H * r_2 \xrightarrow{1/2} 0\}$$

acquire_lock p;

$$\{p \rightsquigarrow \text{Lock } H * r_2 \xrightarrow{1/2} 0 * H\}$$

r := !r + 1;

r2 := !r2 + 1;

$$\{p \rightsquigarrow \text{Lock } H * r_2 \xrightarrow{1/2} 1 * H\}$$

release_lock p;

$$\{p \rightsquigarrow \text{Lock } H * r_2 \xrightarrow{1/2} 1\}$$

Finish up

```
let r = ref 0
let r1 = ref 0
let r2 = ref 0
let p = create_lock()

{p ~ Lock H * r1  $\xrightarrow{1/2}$  0} || {p ~ Lock H * r2  $\xrightarrow{1/2}$  0}
acquire_lock p;
r := !r + 1;
r1 := !r1 + 1;
release_lock p;

{p ~ Lock H * r1  $\xrightarrow{1/2}$  1} || {p ~ Lock H * r2  $\xrightarrow{1/2}$  1}
{p ~ Lock H * r1  $\xrightarrow{1/2}$  1 * r2  $\xrightarrow{1/2}$  1}
acquire_lock p;

{r1  $\xrightarrow{1/2}$  1 * r2  $\xrightarrow{1/2}$  1 * r  $\mapsto$  n * r1  $\xrightarrow{1/2}$  n1 * r2  $\xrightarrow{1/2}$  n2 * 'n = n1 + n2'}
{r1  $\mapsto$  1 * r2  $\mapsto$  1 * r  $\mapsto$  n * 'n = 1 + 1'}
assert (!r == 2);
```

Some remarks

Ghost variables are the basic way of solving this problem

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- same example with an arbitrary number of threads?
- how do we know adding variables really preserves the semantics?
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- allocation of ghost state: for some a any “Resource Algebra”:

$$\text{True} \equiv* \exists \gamma. \gamma \rightsquigarrow \text{Ghost}(a)$$

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- splitting of ghost state: for any a, b in an RA:

$$\gamma \rightsquigarrow \text{Ghost}(a \cdot b) \Leftrightarrow \gamma \rightsquigarrow \text{Ghost}(a) * \gamma \rightsquigarrow \text{Ghost}(b)$$

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$$\gamma \rightsquigarrow \text{Ghost}(a \cdot b) \Leftrightarrow \gamma \rightsquigarrow \text{Ghost}(a) * \gamma \rightsquigarrow \text{Ghost}(b)$$

- validity in RA's e.g. $\text{valid}((r_2 \mapsto 1) \cdot (r_2 \mapsto n_2)) \Rightarrow n_2 = 1$
- heaps are a RA (composition of fractional RA and agreement RA)

Chapter 26: weakest preconditions

Presentation with weakest preconditions

WP are generally preferred to triples, in practice:

- more primitive:

$$\{P\}e\{\Phi\} \equiv P \multimap \text{wp } e \Phi$$

- preconditions become hypotheses, more easily managed

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- more primitive:

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- preconditions become hypotheses, more easily managed

Iris hypotheses go in contexts and can be named, split, rearranged...

$$\begin{array}{ccc} P1 * P2 \multimap \text{wp } e \Phi & \rightarrow & \begin{array}{c} H : P1 * P2 \\ \hline \text{wp } e \Phi \end{array} \end{array} \quad \rightarrow \quad \begin{array}{c} H1 : P1 \\ H2 : P2 \\ \hline \text{wp } e \Phi \end{array}$$

Builtin consequence rule in postconditions

$\text{wp } e \cdot$ is a monotone predicate transformer, so $\text{wp } e \Phi$ is equivalent to

$$\forall \Psi \ (\forall v \ \Phi v \multimap \Psi v) \multimap \text{wp } e \Psi$$

Builtin consequence rule in postconditions

$\text{wp } e \cdot$ is a monotone predicate transformer, so $\text{wp } e \Phi$ is equivalent to

$$\forall \Psi \left((\forall v \ \Phi v \multimap \Psi v) \multimap \text{wp } e \Psi \right)$$

So instead of choosing

$$\{P\}e\{\Phi\} \equiv P \multimap \text{wp } e \Phi$$

the following formulation is preferred:

$$\{P\}e\{\Phi\} \equiv \forall \Psi \ P \multimap (\forall v \ \Phi v \multimap \Psi v) \multimap \text{wp } e \Psi$$

Specifications can be applied directly since Ψ is universally quantified.

Note that separating implication $\multimap$ has no easy “heap entailment” $\triangleright$ counterpart here.

Simplified rules for load, store, alloc

Exercise:

$$\begin{array}{llll} \forall lv\Phi & \dots\dots\dots & \neg * (\dots\dots\dots) & \neg * \text{wp} (!\ell) \Phi \\ \forall lvv'\Phi & \dots\dots\dots & \neg * (\dots\dots\dots) & \neg * \text{wp} (\ell \leftarrow v) \Phi \\ \forall v\Phi & \dots\dots\dots & \neg * (\dots\dots\dots) & \neg * \text{wp} (\text{ref } v) \Phi \end{array}$$

Simplified rules for load, store, alloc

$$\begin{array}{llll} \forall \ell v v' \Phi & \ell \mapsto v' & \multimap (\ell \mapsto v \multimap \Phi()) & \multimap \text{wp}(\ell \leftarrow v) \Phi \\ \forall v \Phi & \top & \multimap (\forall \ell \ell \mapsto v \multimap \Phi \ell) & \multimap \text{wp}(\text{ref } v) \Phi \\ \forall \ell v \Phi & \ell \mapsto v & \multimap (\ell \mapsto v \multimap \Phi v) & \multimap \text{wp}(!\ell) \Phi \end{array}$$

Exemple

$$\{\ell \mapsto 1\} \text{ref } 3 \{\ell'. \ell \mapsto 1 * \ell' \mapsto 3\}$$

Example

$$\{\ell \mapsto 1\} \text{ref } 3 \{\ell'. \ell \mapsto 1 * \ell' \mapsto 3\}$$

in other words:

$$\forall \Phi (\ell \mapsto 1) \multimap (\forall \ell'. \ell \mapsto 1 * \ell' \mapsto 3 \multimap \Phi \ell') \multimap \text{wp}(\text{ref } 3) \Phi$$

Example

$$\{\ell \mapsto 1\} \text{ref } 3 \{\ell'. \ell \mapsto 1 * \ell' \mapsto 3\}$$

in other words:

$$\forall \Phi (\ell \mapsto 1) \multimap (\forall \ell'. \ell \mapsto 1 * \ell' \mapsto 3 \multimap \Phi \ell') \multimap \text{wp}(\text{ref } 3) \Phi$$

after `iIntros (Φ) "H1 HΦ"` you have:

"H1" : $l \mapsto \#1$

"HΦ" : $\forall l' : \text{loc}, l \mapsto \#1 * l' \mapsto \#3 \multimap \Phi \#l'$

-----*

WP ref #3 {{ v, Φ v }}

Example

$$\{\ell \mapsto 1\} \text{ref } 3 \{ \ell'. \ell \mapsto 1 * \ell' \mapsto 3 \}$$

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-----*

WP ref #3 { { v, Φ v } }

applying the rule for allocation, we get:

"H1" : $1 \mapsto \#1$

"HΦ" : $\forall l' : \text{loc}, 1 \mapsto \#1 * l' \mapsto \#3 \multimap \Phi \#l'$

-----*

$\forall l_0 : \text{loc}, l_0 \mapsto \#3 \multimap \Phi \#l_0$

after `iIntros (l') "Hl'"` we get:

"Hl" : $l \mapsto \#1$

"HΦ" : $\forall l'0 : \text{loc}, l \mapsto \#1 * l'0 \mapsto \#3 -* \Phi \#l'0$

"Hl'" : $l' \mapsto \#3$

-----*

$\Phi \#l'$

after iIntros (l') "Hl'" we get:

"Hl" : $l \mapsto \#1$

"HΦ" : $\forall l'0 : \text{loc}, l \mapsto \#1 * l'0 \mapsto \#3 -* \Phi \#l'0$

"Hl'" : $l' \mapsto \#3$

-----*

$\Phi \#l'$

after iApply "HΦ" we get:

"Hl" : $l \mapsto \#1$

"Hl'" : $l' \mapsto \#3$

-----*

$l \mapsto \#1 * l' \mapsto \#3$

after iIntros (l') "Hl'" we get:

"Hl" : $l \mapsto \#1$

"HΦ" : $\forall l'0 : \text{loc}, l \mapsto \#1 * l'0 \mapsto \#3 -* \Phi \#l'0$

"Hl'" : $l' \mapsto \#3$

-----*

$\Phi \#l'$

after iApply "HΦ" we get:

"Hl" : $l \mapsto \#1$

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-----*

$l \mapsto \#1 * l' \mapsto \#3$

iFrame solves the goal.

after iIntros (l') "Hl'" we get:

"Hl" : $l \mapsto \#1$

"HΦ" : $\forall l'0 : \text{loc}, l \mapsto \#1 * l'0 \mapsto \#3 \text{ -* } \Phi \#l'0$

"Hl'" : $l' \mapsto \#3$

-----*

$\Phi \#l'$

after iApply "HΦ" we get:

"Hl" : $l \mapsto \#1$

"Hl'" : $l' \mapsto \#3$

-----*

$l \mapsto \#1 * l' \mapsto \#3$

iFrame solves the goal.



Iris is an affine logic, it can throw away hypotheses.



Chapter 27: modalities

Persistently modality $\Box$

$\Box P$ is roughly equivalent to $P * \text{'}P \text{ is duplicable'}$ (or “persistent”)

$$\Box P \triangleright P$$

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$$\Box P \triangleright P \qquad \Box P \triangleright \Box P * P$$

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$$\ulcorner P \urcorner \triangleright \Box \ulcorner P \urcorner$$

Persistently modality $\Box$

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$$\Box P \triangleright P \qquad \Box P \triangleright \Box P * P \qquad \Box P \triangleright \Box P * \Box P \qquad \Box P \triangleright \Box P * P * P$$

$$\text{'}P \text{' } \triangleright \Box \text{'}P \text{' } \qquad \Box(\ell \mapsto 1) \triangleright \text{'}False \text{'}$$

Persistently modality $\Box$

$\Box P$ is roughly equivalent to $P * \text{'}P \text{ is duplicable'}$ (or “persistent”)

$$\Box P \triangleright P \quad \Box P \triangleright \Box P * P \quad \Box P \triangleright \Box P * \Box P \quad \Box P \triangleright \Box P * P * P$$

$$\text{'}P \text{' } \triangleright \Box \text{'}P \text{' } \quad \Box(\ell \mapsto 1) \triangleright \text{'False' } \quad \Box(\ell \xrightarrow{q} 1) \triangleright \text{'q=0' (if even allowed)}$$

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$$\Box P \triangleright P \quad \Box P \triangleright \Box P * P \quad \Box P \triangleright \Box P * \Box P \quad \Box P \triangleright \Box P * P * P$$

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$$\{P\}e\{\Phi\} \equiv \Box(\forall \Psi \ P \multimap (\forall v \ \Phi v \multimap \Psi v) \multimap \text{wp } e \ \Psi)$$

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$\Box P$ is roughly equivalent to $P * \text{'}P \text{ is duplicable'}$ (or “persistent”)

$$\Box P \triangleright P \quad \Box P \triangleright \Box P * P \quad \Box P \triangleright \Box P * \Box P \quad \Box P \triangleright \Box P * P * P$$

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$$\{P\}e\{\Phi\} \equiv \Box(\forall \Psi \ P \multimap (\forall v \ \Phi v \multimap \Psi v) \multimap \text{wp } e \ \Psi)$$

Persistent resources can be shared between threads:

$$\frac{\{P_1 * \Box P\} e_1 \{Q_1\} \quad \{P_2 * \Box P\} e_2 \{Q_2\}}{\{P_1 * P_2 * \Box P\} (|e_1, e_2|) \{Q_1 \star Q_2\}}$$

Persistently modality $\Box$

$\Box P$ is roughly equivalent to $P * \text{'}P \text{ is duplicable'}$ (or “persistent”)

$$\Box P \triangleright P \quad \Box P \triangleright \Box P * P \quad \Box P \triangleright \Box P * \Box P \quad \Box P \triangleright \Box P * P * P$$

$$\text{'}P \text{' } \triangleright \Box \text{'}P \text{' } \quad \Box(\ell \mapsto 1) \triangleright \text{'False' } \quad \Box(\ell \xrightarrow{q} 1) \triangleright \text{'q=0' } \text{ (if even allowed)}$$

$$\{P\}e\{\Phi\} \equiv \Box(\forall \Psi \ P \multimap (\forall v \ \Phi v \multimap \Psi v) \multimap \text{wp } e \ \Psi)$$

Persistent resources can be shared between threads:

$$\frac{\{P_1 * \Box P\} e_1 \{Q_1\} \quad \{P_2 * \Box P\} e_2 \{Q_2\}}{\{P_1 * P_2 * \Box P\} (|e_1, e_2|) \{Q_1 \star Q_2\}}$$

Some ghost resources are persistents (e.g. to indicate a task is done), some are not (e.g. to provide a thread with an information it can consume).

Later modality $\triangleright$

$\triangleright P$ (“later P ”) can be thought as “ P holds after one reduction step”.

$\triangleright$ is a modality ($\triangleright P$), $\triangleright$ is a binary predicate ($P \triangleright Q$)

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$$\triangleright^n \text{False} \vdash P \text{ iff } P \text{ holds for } n \text{ steps} \qquad \vdash \exists k. \triangleright^k \text{False}$$

More later

The **Löb rule** is very convenient for partial correctness:

$$\frac{Q \wedge \triangleright P \vdash P}{Q \vdash P}$$

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Final definition of triples:

$$\{P\}e\{\Phi\} \equiv \Box(\forall \Psi \ P \multimap \triangleright(\forall v \ \Phi v \multimap \Psi v) \multimap \text{wp } e \ \Psi)$$

Complete set of rules: [Lecture Notes on Iris](#)

Invariants

New construct $\boxed{R}^\iota$: duplicable, but the resource R is lost at allocation:

$$\boxed{R}^\iota \vdash \square \boxed{R}^\iota \qquad \frac{S, \boxed{R}^\iota \vdash \{P\}e\{Q\}}{S \vdash \{\triangleright R * P\}e\{Q\}} \text{INV-ALLOC}$$

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Invariant resources can be accessed but must be preserved:

$$\frac{e \text{ is atomic} \quad S, \boxed{R}^\ell \vdash \{\triangleright R * P\}e\{\triangleright R * Q\}}{S, \boxed{R}^\ell \vdash \{P\}e\{Q\}} \text{INV-OPEN}$$

(note: easy to have inconsistent invariants rules, one needs to add stratification not to nest openings)

Locks

Locks can be derived from Compare-And-Swap:

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let create_lock () = ref false
let acquire p = if CAS p false true then () else acquire p
let release p = p := false
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$$p \rightsquigarrow \text{Lock}(R, \gamma) \equiv \exists \iota. \left[p \mapsto \text{true} \vee p \mapsto \text{false} * R * \gamma \rightsquigarrow \text{Ghost}(K) \right]^\iota$$

with resource algebra $(\varepsilon, K, \perp)$ s.t. $x \cdot \varepsilon = \varepsilon \cdot x = x$ otherwise $x \cdot y = \perp$.

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$$\forall R. \quad \{R\} (\text{create_lock } ()) \{ \lambda p. \exists \gamma. p \rightsquigarrow \text{Lock}(R, \gamma) \}$$

$$\forall p R. \quad \{p \rightsquigarrow \text{Lock}(R, \gamma)\} (\text{acquire_lock } p) \{ \lambda _. R * p \rightsquigarrow \text{Lock}(R, \gamma) \}$$

$$\forall p R. \{R * p \rightsquigarrow \text{Lock}(R, \gamma)\} (\text{release_lock } p) \{ \lambda _. p \rightsquigarrow \text{Lock}(R, \gamma) \}$$

Conclusion

Some separation logic features:

- tree-like structures, some sharing,
- abstracting intermediate pointers, internal structure
- higher-order representation predicates
- first-class functions, local state
- ghost state, invariants
- concurrency (rich literature, including weak memory models)
- effects (rich literature here too, e.g. effect handlers)

Bibliography: comprehensive [lecture notes on Iris](#)

More slides if more time

Chapter 27

Read-only permissions

Motivation for read-only permissions

What we currently need to write:

$$\{a_1 \rightsquigarrow \text{Array } L_1 * a_2 \rightsquigarrow \text{Array } L_2\}$$

$$(\text{concat } a_1 \ a_2)$$

$$\{\lambda a_3. a_3 \rightsquigarrow \text{Array } (L_1 \text{ ++ } L_2) * a_1 \rightsquigarrow \text{Array } L_1 * a_2 \rightsquigarrow \text{Array } L_2\}$$

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What we wish to write:

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More than syntactic sugar:

- we wish “ro” to enforce no write operations,
- we wish to allow aliasing of read-only arguments.

Generic read-only modifier

Extension of the logic with a modifier $\text{RO}(H)$ that applies to any H .

$$a \overset{\text{ro}}{\rightsquigarrow} \text{Array } L \equiv \text{RO}(a \rightsquigarrow \text{Array } L)$$

$\text{RO}(H)$ is duplicable and never mentioned in post-conditions.

$$\frac{}{\text{RO}(H) \triangleright \text{RO}(H) * \text{RO}(H)} \text{DUP-RO}$$

$$\frac{}{\{\text{RO}(l \mapsto v)\} (\text{get } l) \{\lambda x. \ulcorner x = v \urcorner\}} \text{GET-RO}$$

Read-only frame rule

$\text{RO}(H)$ is introduced on frame:

$$\frac{\{H * \text{RO}(H')\} t \{Q\} \quad \text{no-ro-in } H'}{\{H * H'\} t \{Q \star H'\}} \text{FRAME-RO}$$

Read-only sequencing rule

$$\frac{\{H\} t_1 \{Q'\} \quad \{Q'()\} t_2 \{Q\}}{\{H\} (t_1 ; t_2) \{Q\}} \text{SEQ}$$

$$\frac{\{H * \text{RO}(H')\} t_1 \{Q'\} \quad \{Q'() * \text{RO}(H')\} t_2 \{Q\}}{\{H * \text{RO}(H')\} (t_1 ; t_2) \{Q\}} \text{SEQ-RO}$$

$$\frac{\{H\} t_1 \{Q'\} \quad \{Q'() * H'\} t_2 \{Q\}}{\{H * H'\} (t_1 ; t_2) \{Q\}} \text{SEQ-FRAME}$$

RO in practice

$$\begin{aligned} & \{ \text{RO}(a_1 \rightsquigarrow \text{Array } L_1) * \text{RO}(a_2 \rightsquigarrow \text{Array } L_2) \} \\ & (\text{concat } a_1 \ a_2) \\ & \{ \lambda a_3. \ a_3 \rightsquigarrow \text{Array } (L_1 \mathbin{++} L_2) \} \end{aligned}$$

Parallel rule needs read-only permissions

$$\frac{\{H_1\} t_1 \{Q_1\} \quad \{H_2\} t_2 \{Q_2\}}{\{H_1 * H_2\} (|t_1, t_2|) \{Q_1 \star Q_2\}} \text{PARALLEL}$$

Compute: $u[a[0]] + u[a[1]] + \dots + u[a[n-1]]$.

```
map_reduce (fun x -> u.(a.(x))) 0 (+) 0 n
```

The ownership of the array `u` is needed in both branches.

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$$\frac{\{H_1 * \text{RO}(H_3)\} t_1 \{Q_1\} \quad \{H_2 * \text{RO}(H_3)\} t_2 \{Q_2\}}{\{H_1 * H_2 * \text{RO}(H_3)\} (|t_1, t_2|) \{Q_1 \star Q_2\}} \text{PARALLEL-RO}$$